GSAS Micro Systems Pvt Ltd (GSASMSPL) is India’s Premier Distributor for Total Phase products. Total Phase is a leading provider of embedded systems development tools for engineers all over the world. For years, Total Phase has developed solutions that have become tools of choice for Fortune 500 companies, small businesses, and research institutions alike.

**Aardvark I2C/SPI Host Adapter**

Most popular product, the Aardvark I2C/SPI Host Adapter, is a fast and powerful USB-to-I2C/SPI Emulator. With its ability to emulate a Host or Device, communicate in I2C or SPI, this host adapter is a versatile tool well-suited for a variety of applications.

**Key Features**

- USB to I2C/SPI Interface
- Host or Device emulation
- EEPROM/Flash programming
- I2C speeds up to 800 kHz
- SPI speeds up to 8 MHz
- 6 GPIOs available

**Beagle I2C/SPI Protocol Analyzer**

The Beagle I2C/SPI Protocol Analyzer is the ideal tool for debugging and monitoring traffic on your I2C, SPI, or SMBus/MDIO based applications. The Beagle analyzer provides a high performance bus monitoring solution in a small, portable package. It provides fast interactive, real-time visibility into the protocol layer of your embedded system.

**Key Features**

- Real-Time Non-Intrusive Monitoring
- I2C (SMBus supported): Up to 5 MHz
- SPI: Up to 24 MHz
- MDIO (Clause 22 and 45): Up to 2.5 MHz SMBus beta Version

**Beagle USB Protocol Analyzers**

**Beagle USB 480 Protocol Analyzer**

The Beagle USB 480 Protocol Analyzer is a low-cost, non-intrusive High-speed USB 2.0 bus monitor that includes real-time USB class-level decoding.

**Beagle USB 480 Power Protocol Analyzer**

The Beagle USB 480 Power Protocol Analyzer is a low-cost, non-intrusive High-speed USB 2.0 bus monitor that includes VBus Monitoring for Voltage and Current and real-time USB class-level decoding.

**Beagle USB 5000v2 Protocol Analyzer**

The Beagle USB 5000 Super Speed Protocol Analyzer is a world-class USB 3.0 bus monitor that provides real-time interactive capture and analysis of USB 3.0 and USB 2.0 traffic (up to 5 Gbps).
Promira Serial Platform

Promira Serial Platform is the world’s first I2C/SPI master or slave with eSPI simulator and protocol analysis capabilities. Built on a completely new FPGA-based architecture, Promira platform is the latest tool in Total Phase’s established line of cost-effective and easy-to-use embedded solutions.

**Key Features**

- USB/Ethernet to I2C/SPI/eSPI Interface
- Dual and Quad I/O SPI/eSPI
- Integrated level shifting (0.9 – 3.45 V, 5 V)
- Ethernet and high-speed USB interface
- Up to 16 GPIOs
- Up to 8 slave selects
- Windows, Linux, Mac OS X
- API Support
- 32-and 64-bit support for C/C++/C#, Python, .NET, VB.Net
- Create custom applications using flexible, powerful, and well-documented Promira API

Advanced Cable Tester v2

Total Phase Advanced Cable Tester v2 is the quickest and most convenient way to comprehensively test USB and video cables. Combining blazing fast performance with a low cost per test and a rugged design, the Advanced Cable Tester v2 enables rapid spot-checking of cables, easy-to-understand reports, with 100% test coverage for lab and production environments at a fraction of the price, time, and labor, versus other solutions.

It provides:

- Short testing - Dynamic visualization of test results
- Open testing - Dynamic visualization of test results
- Continuity testing - Dynamic visualization of test results
- DC resistance measurement
- Signal integrity testing of data lines to 12.8 Gbps/channel
- E-Marker verification

(Specifications are subject to change without prior notice)

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<table>
<thead>
<tr>
<th>Protocol</th>
<th>Frequency</th>
<th>Master Maximum</th>
<th>Slave Maximum</th>
</tr>
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<tbody>
<tr>
<td>I2C</td>
<td>1MHz - 3.4MHz</td>
<td>1MHz - 3.4MHz</td>
<td></td>
</tr>
<tr>
<td>SPI</td>
<td>12.5MHz - 80MHz</td>
<td>8MHz - 20MHz</td>
<td></td>
</tr>
<tr>
<td>eSPI</td>
<td>20MHz - 66MHz</td>
<td>N/A</td>
<td></td>
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</tbody>
</table>

Analyzer

<table>
<thead>
<tr>
<th>Protocol</th>
<th>Frequency</th>
<th>Feature</th>
</tr>
</thead>
<tbody>
<tr>
<td>eSPI</td>
<td>20MHz - 66MHz</td>
<td>2 chips selects with reset alert</td>
</tr>
</tbody>
</table>

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