



GSAS Micro Systems Pvt. Ltd (GSASMSPL) has partnered with Zeroplus Technology Co. Ltd to bring forth GSAS LAPx Series Logic Cum protocol analyzers, a range of advanced logic cum protocol analyzers. ZeroPlus has in-depth knowledge of the industry and possesses the core technology in IC Design. They specialize in PC-based logic analyzers and strive to design innovative products to meet customers demands.

GSAS LAPx Series logic Cum protocol analyzers captures the bus signal and helps analyze the time stamps of the design. It supports high-speed clock rates, sophisticated triggering, compression of data, etc. These functions in the LAPx logic Cum protocol analyzers aid design engineers in identifying and troubleshooting faults thus making it an indispensable tool for them.

Supported Protocols

Automotive	PC System	Memory	Digital Audio	IC Interface
CAN 2.OB, DSI Bus, FLEX RAY 2.1A, LIN 2.1, MVB, WTB	Compact Flash 4.1, Low Pin Count, LPC - SERIQ, LPT, PCI, PECL, PS/2, SD2.0/SDIO,SVID, UART,USB 2.0...etc	1-WIRE, 3-WIRE, 12C, 12C(EEPROM 24L),MICROWIRE (EEPROM 93C), CS61/24LCS62,SPI, UNI/O....etc	AC97, HD Audio, HDMI CEC, 12S, PCM, S/PDIF, ST Bus	HPI,JTAG 2.0, MCU - 51 DECODE, MICROWIRE, SIGNIA 6210, SLE4442, ST7669, SSI Interface...etc

Optoelectronics	Basic Logic Application	Infrared rays	Power	Wireless	Others
7 - SEGMENT LED, CCIR656, DALI Interface, DM 14/ DM 15,DMX512, LCD12864, LCD1602,LG4572... etc	ARITHMETIC LOGIC, DIGITAL LOGIC, JK - FLIP - FLOP UP DOWN COUNTER	IRDA, NEC PD6122, Philips RC-5, Philips RC-6, PT2262/PT2272	HDQ, PM Bus 1.1, SDQ, 5Mbus 2.0	DIGRF, ISO7816 UART,MIL, MANCHESTER, MILLER,MODIFIED MILLER, SWP, WIEGAND....etc	BDM, DS1302, GPIB, Mod Bus, DS18B20, DSA Interface, YK-5, SHT11, PROFIBUS, OPENTHERM 2.2, MODIFIED SPI...etc

Specifications of GSAS LAP Series Logic Analyzers

GSAS LAP - C Series							
Channel Mode		16032	16064	16128	162000	32128	322000
Channel		16				32	
Sample Rate	Timing Mode	100Hz ~ 100/MHz			100Hz ~ 200MHz		
	State Mode	Max 75MHz			100MHz		
Memory	Memory	512K bits	1M bits	4M bits	64M bits	4M bits	64M bits
	Memory Depth (Per Channel)	32K bits	64K bits	128K bits	2M bits	128K bits	2M bits
Trigger	Trigger Channel	16 Channnels				32 Channels	
	Trigger Level	1 Level					
Protocol Packet Trigger		Parallel					Serial / Parallel



GSAS LAP - C Pro Series				
Specification	16064M	32064M	32128M	32256M
Channels	16	32		
Memory Depth Per Channel	8ch/128Mb 16ch/64Mb	8ch/256Mb 16ch/128Mb 32ch/64Mb	8ch/512Mb 16ch/256Mb 32ch/128Mb	8ch/1Gb 16ch/512Mb 32ch/256Mb
Total Memory	1Gb	2Gb	4Gb	8Gb
Maximum Sampling Rate Internal	8ch/1GHz 16ch/500MHz	8ch/1GHz 16ch/500MHz 32ch/500MHz		
Maximum Sampling Rate External	250MHz(Must be with high speed adaptor kit)			
Range of the Trigger Voltage	-6V ~ +6V			
Resolution of Trigger Voltage	10mV/Step			
Pattern Generator	Option	Standard		
Operating System	Windows 10 / 8.1 / 7 (32 or 64 bit)			
Interface / Power supply	USB3.0 / DC 5V (Max.3W)			
Dimensions / Weight	126 x 95 x 25 mm / 160g			

GSAS LAP - F Series				
Product Name	LAP-F1404M	LAP-F14064M	LAP-F1644M	LAP-F16464M
Channels	40		64	
Operating System	Windows 8.1(Recommended) / Windows 7 32-bit or 64-bit			
Transmission	USB 3.0(2.0 compatible)			
Sample Rate	Internal (Timing) Max	1 GHz		
	External (State) Max	200MHz(Dual-Edge)		



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Bus Expert II is a powerful tool for measuring and debugging eMMC5.1 and/or SD3.0. To make sure your measuring and debugging is as fast, accurate and precise as possible, we've equipped this 32 channels instrument with 2G memory depth in each channel, and the all-new-designed Active Probe makes this even better by significantly reducing the signal distortion from DUT.

To make sure you are not missing any bit of important signals, the Lossless Long Time Record in Protocol Analyzer mode feature will document everything.

(Specifications are subject to change without prior notice)

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BENGALURU	CHENNAI	DELHI	HYDERABAD	MUMBAI & PUNE	VIZAG & VIJAYAWADA
sales@gsasmspl.com +91 98450 82950	tn@gsasmspl.com +91 98450 19071	delhi@gsasmspl.com +91 98450 55228	ts@gsasmspl.com +91 98450 19029	mh@gsasmspl.com +91 98450 42976	ap@gsasmspl.com +91 98450 25622

GSAS MICRO SYSTEMS PVT. LTD

147/107, 6th Main, Mahalakshmpuram, Bengaluru - 560086
sales@gsasmspl.com | www.gsasindia.com
Ph: +91 80 2349605/52 +91 80 23499000

